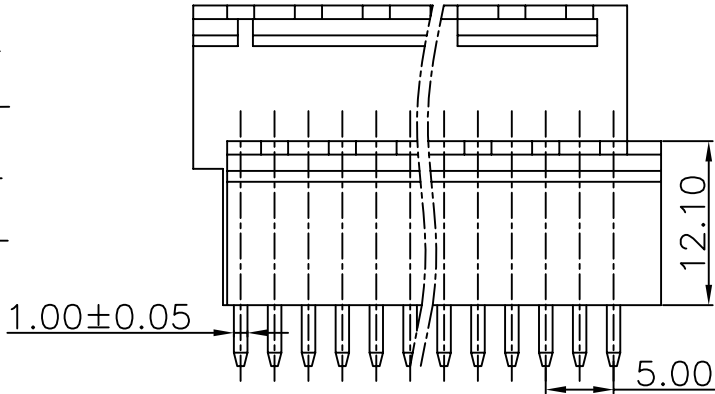
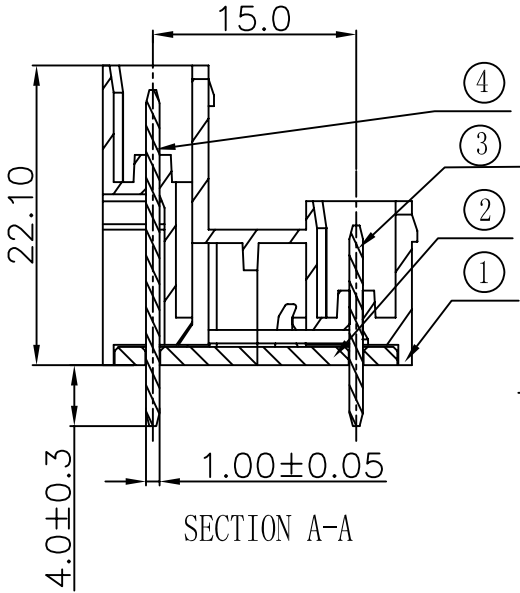
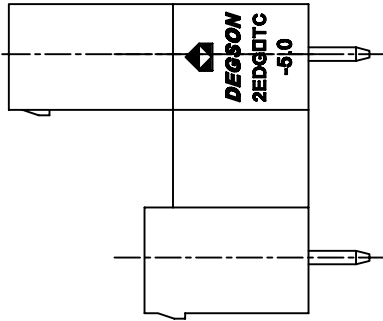
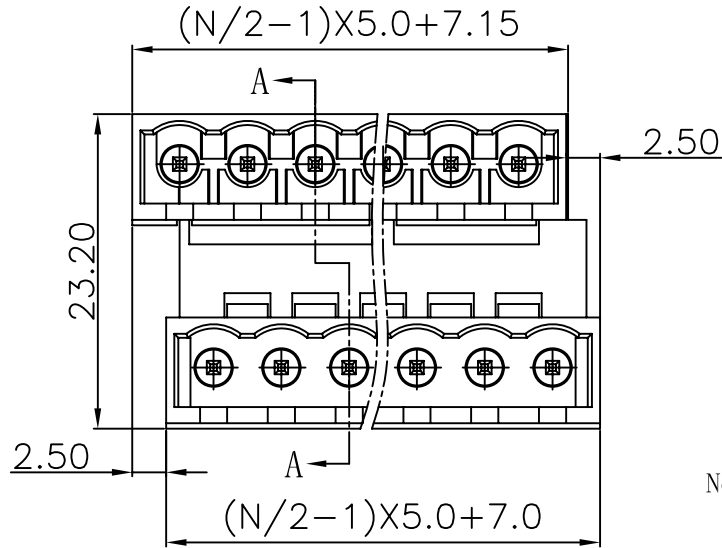
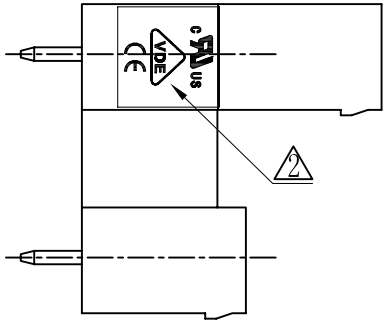


REVISION RECORD					
REV	ECO	DESCRIPTION	DRFT	DATE	
△		品名、料号增加环保标示“(H)”	LJH	12/11/07*	
△		增加VDE刻字	Zengy	11/12/08*	



- Notes:
- Pitch:5.0mm ,Poles: One-block for 4-24P
Inter locking for 24-48P
 - Rated voltage/current:UL 300V/15A
VDE 320V/10A
 - Operation temperatures:-40℃~+105℃
 - Withstanding Voltage:AC2000V/1Min
 - Insulation Resistance:5000MΩ /DC1000V
 - Undimensioned Tolerances:

Pitch range in mm			Nominal dimension range in mm					—
to 6	over 6 to 10	over 10 to 24	to 30	over 30 to 60	over 60 to 100	over 100 to 150	over 150	
±0.10	±0.15	±0.25	±0.2	±0.3	±0.5	±0.7	±1.0	0.05/10

4	2EDGVT-long pin	1032B00010 (H)	XX/2	Brass	SnCe
3	2EDGV-pin	1007B00030 (H)	XX/2	Brass	SnCe
2	2EDGVT-5.0-Cover	1032C0Y (XX/2 & XX/2 +1) A (H)	1	PA66	>24P
	2EDGVT-5.0-Cover	1032C0Y (XX/2 & XX/2 -1) A (H)	1	PA66	
	2EDGVT-5.0 Cover	1032C0YXXA (H)	1	PA66	≤24P

1	2EDGVT-5.0 housing(Right foot)	1072A0Y (XX/2 & XX/2 +1) C (H)	1	PA66	>24P
	2EDGVT-5.0 housing(Left foot)	1072A0Y (XX/2 & XX/2 -1) B (H)	1	PA66	
	2EDGVT-5.0-Housing	1072A0YXXA (H)	1	PA66	≤24P
ITEM	NAME OF PART	NO. OF PART	Q"TY	MATERIAL	NOTES

DETTACHED LISTS	MM (INCH)	DFTO Apple	DATE 06/21/06*	<div>DEGSN®</div> 宁波高正电子有限公司 DEGSN ELECTRONICS CO., LTD	
	TOLERANCES EXCEPT AS NOTED	CHKD	DATE		
		MFO	DATE		
		APPVL	DATE		
	MM .0 ± .00 ± .000 ±			NAME 2EDGVT-5.0-XXP-1Y-00A (H)	
	ANGLES ± 1°	G.E :		DRAWING NO. 2EDGVT-5.0-E01	SIZE A3
	THIRD ANGLE PROJECTION	FINISH :		/PART NO.	REV T2 11/12/08*
		SCALE :		DO NOT SCALE DRAWING	SHEET 1 OF 1