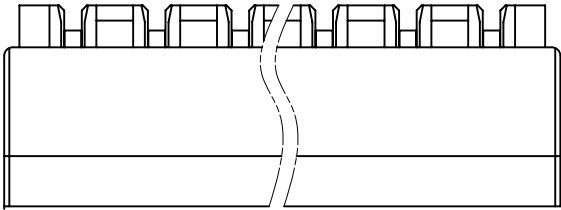


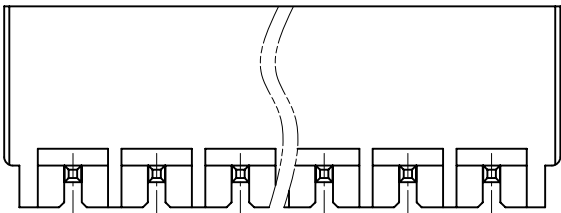
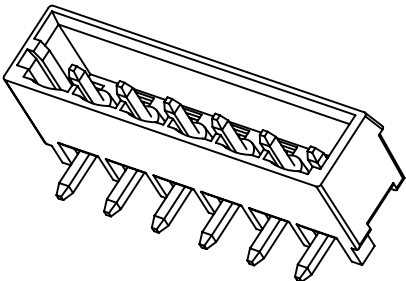
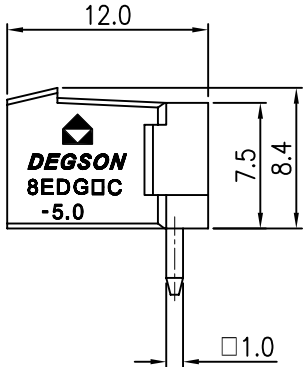
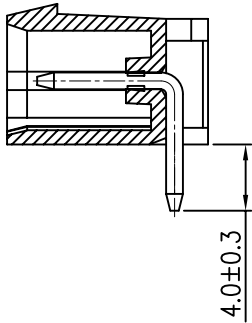
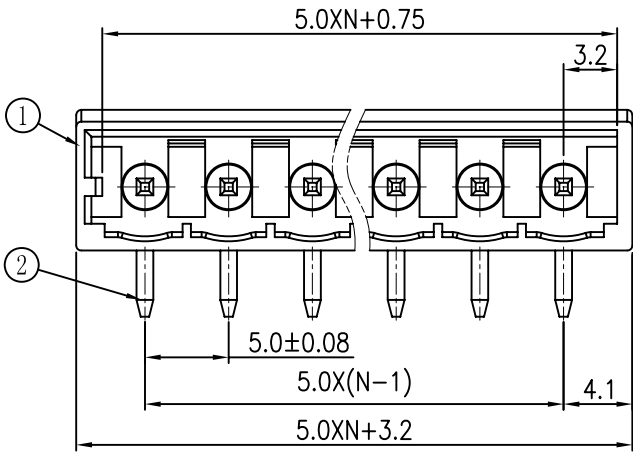
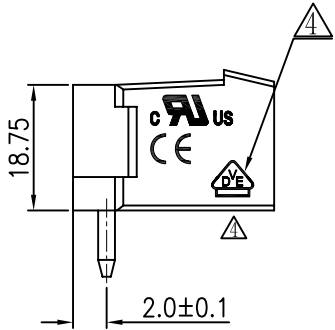
PCB LAYOUT (REF)



- Notes:
1. Pitch: 5.0mm , Poles: 2-24P
 2. Rated voltage/current: UL 300V/15A
VDE 320V/20A
 3. Operation temperatures: -40℃~+105℃
 4. Withstanding Voltage: AC2000V/1min
 5. Insulation Resistance: 5000MΩ/DC1000V
 6. Undimensioned Tolerances:

REVISION RECORD				
REV	ECN	DESCRIPTION	DRFT	DATE
△		增加UL、CE刻字	CSX	02/26/08
△		修改视图	LJH	05/16/09
△	10-0029	全面修改塑件结构	CW	03/24/10
△	10-0325	增加“VDE”刻字	Jinbai, Lan	09/29/10

Pitch range in mm		Nominal dimension range in mm						
to 6	over 6 to 10	over 10 to 24	to 30	over 30 to 60	over 60 to 100	over 100 to 150	over 150	
±0.10	±0.15	±0.25	±0.2	±0.3	±0.5	±0.7	±1.0	0.05/0



2	2EDGR-Pin header	1008B00040 (H)	XX	Brass	SnCe plated
1	8EDGVC-5.0 Housing	1089A41XXA (H)	1	PA66	UL94V-0
ITEM	NAME OF PART	NO. OF PART	Q'TY	MATERIAL	NOTES
TOLERANCES EXCEPT AS NOTED		DFTO ZYH	DATE 06/08/07	宁波高正电子有限公司 DEGSON ELECTRONICS CO., LTD	
MM (INCH)		CHKD	DATE		
.0 ±		MFO	DATE		
.00 ±		APPVL	DATE		
.000 ±		MATERIAL :		NAME	
ANGLES ± 1°		G.E :		8EDGRC-5.0-XXP-11-01A(H)	
THIRD ANGLE PROJECTION		FINISH :		DRAWING NO. 8EDGRC-5.0-E01	
		SCALE : 1:1		/PART NO.	
		DO NOT SCALE DRAWING		SHEET 1 OF 1	